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# AMENDMENT TRANSMITTAL LETTER

Docket No.  
R2180.0188/P188

Application No.  
10/763,255-Conf. #7512

Filing Date  
January 26, 2004

Examiner  
P. M. Chung

Art Unit  
2133

Applicant(s): Takamitsu Yamada et al.

Invention: SEMICONDUCTOR INTEGRATED CIRCUIT AND SCAN TEST METHOD THEREFOR

## TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	24	- 30 =	0	x 50.00	0.00
Independent Claims	8	- 6 =	2	x 210.00	420.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					420.00

☒ Large Entity

☐ Small Entity

☐ No additional fee is required for this amendment.

☐ Please charge Deposit Account No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_.  
A duplicate copy of this sheet is enclosed.

☐ A check in the amount of \$ \_\_\_\_\_ to cover the filing fee is enclosed.

☒ Payment by credit card. Form PTO-2038 is attached.

☒ The Director is hereby authorized to charge and credit Deposit Account No. 04-1073  
as described below. A duplicate copy of this sheet is enclosed.

☒ Credit any overpayment.

☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

  
Thomas J. D'Amico  
Attorney/Agent Reg. No.: 28,371

Dated: 12/6/07

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